



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Robert M. Steinhoff, et al.

Serial No: 10/690,006

TI-35703

Art Unit: TBD

Filed: 10/21/03

Examiner: TBD

For: ESD Protection for Integrated Circuits

INFORMATION DISCLOSURE STATEMENT

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Applicants wish to bring to the attention of the Patent and Trademark Office the information noted on the enclosed PTO-1449. A copy of the noted reference is enclosed herewith.

Respectfully submitted,

  
Rose Alyssa Keagy  
Attorney for Applicants  
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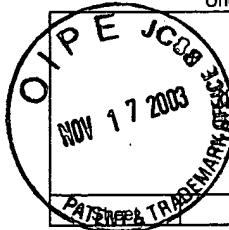
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Complete If Known	
Application Number	10/690,006
Filing Date	10/21/03
First Named Inventor	Robert M. Steinhoff et al.
Group Art Unit	TBD
Examiner Name	TBD
Attorney Docket No.	TI-35703

**U.S. PATENT DOCUMENTS**

Exam. Initials*	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code <sup>2</sup> (if known)			
AA	5,539,233			Amerasekera et al.	7/23/96	
AB						
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**FOREIGN PATENT DOCUMENTS**

Exam. Initials*	Cite No. <sup>1</sup>	Foreign Patent Document			Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
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**OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS**

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